

FIGURE 1

FIG. 2 is a block diagram of a thermal control system 16, showing a first thermal control loop 34 and a second thermal control loop 34'.

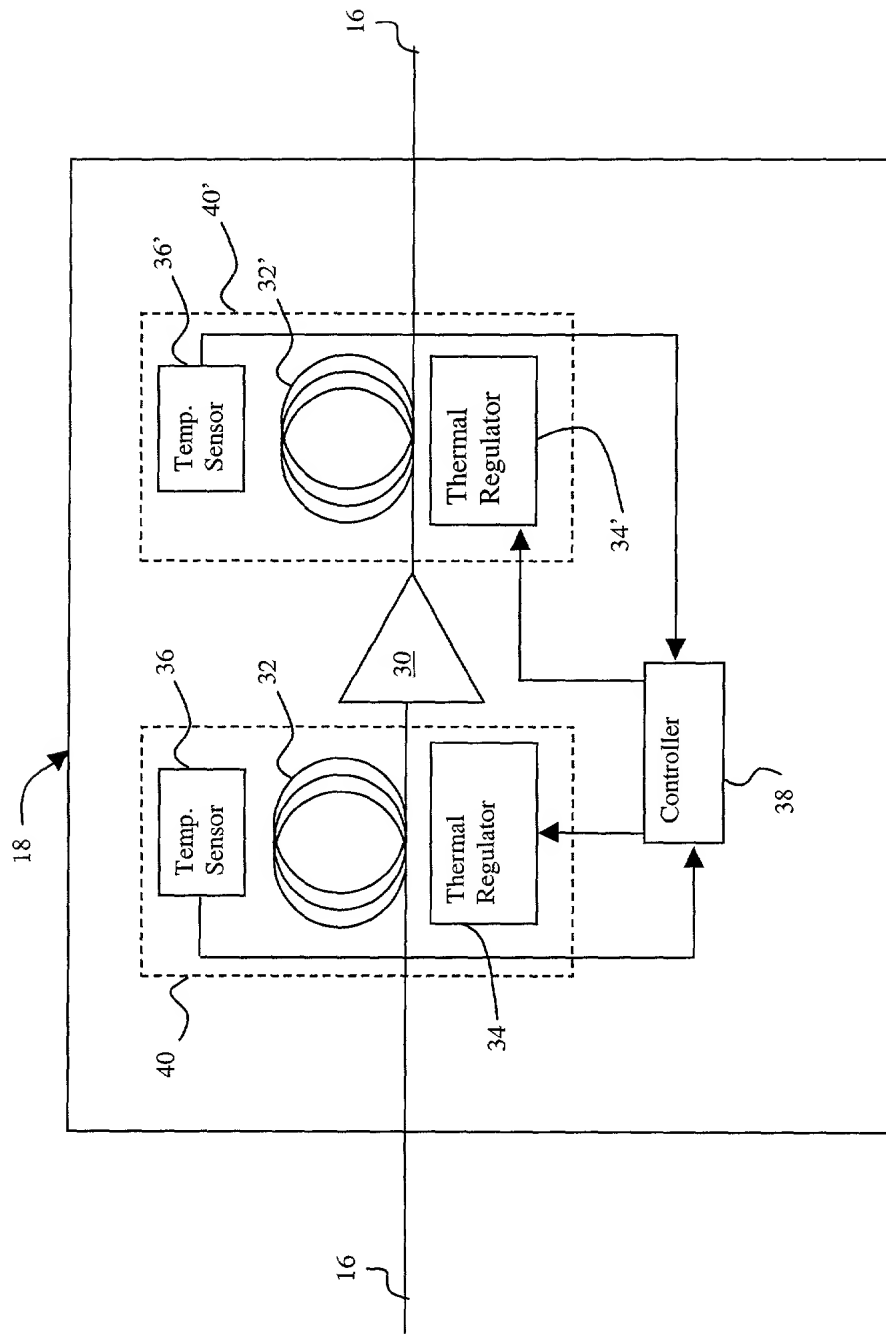
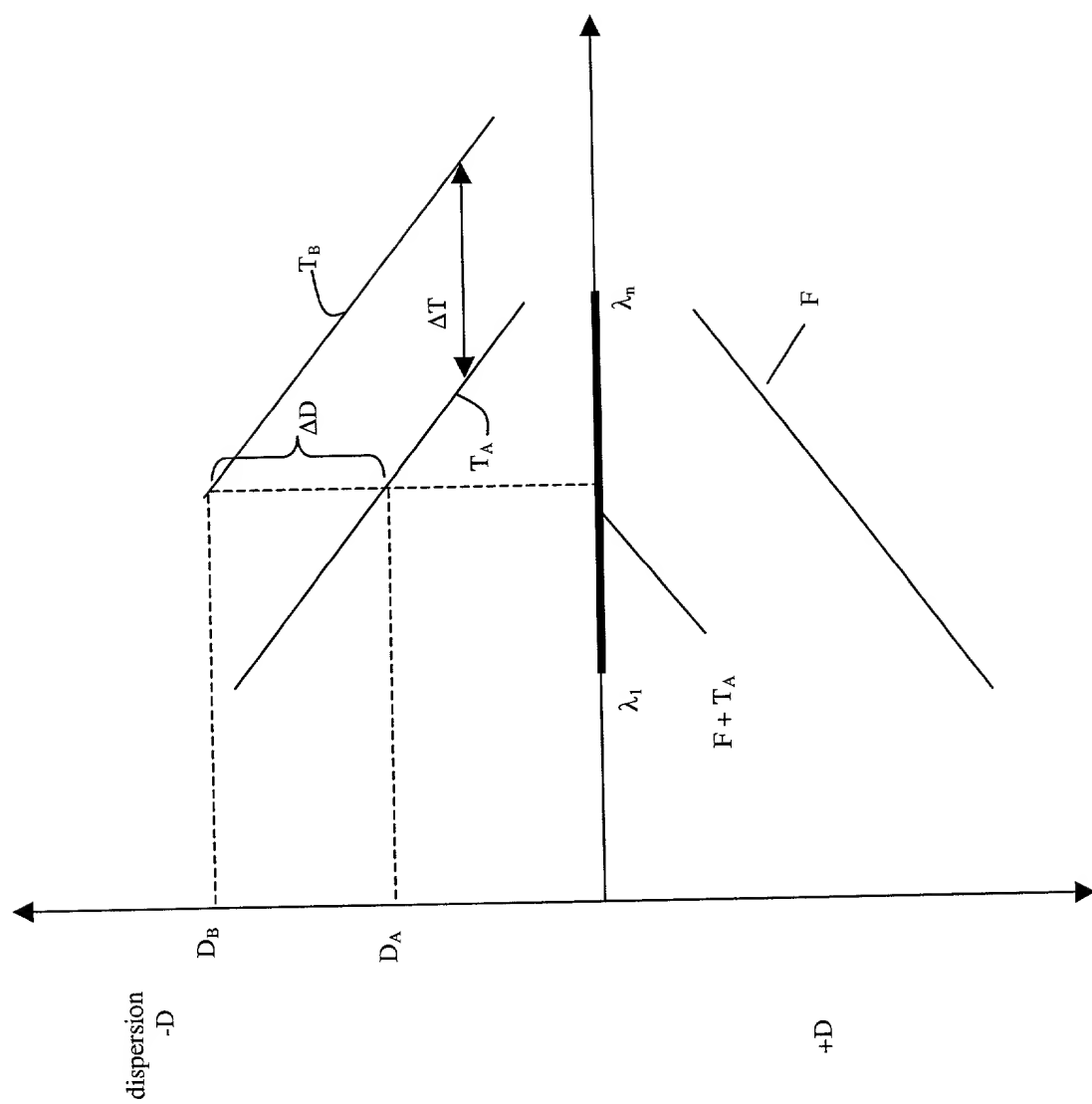


FIGURE 2

[illegible]

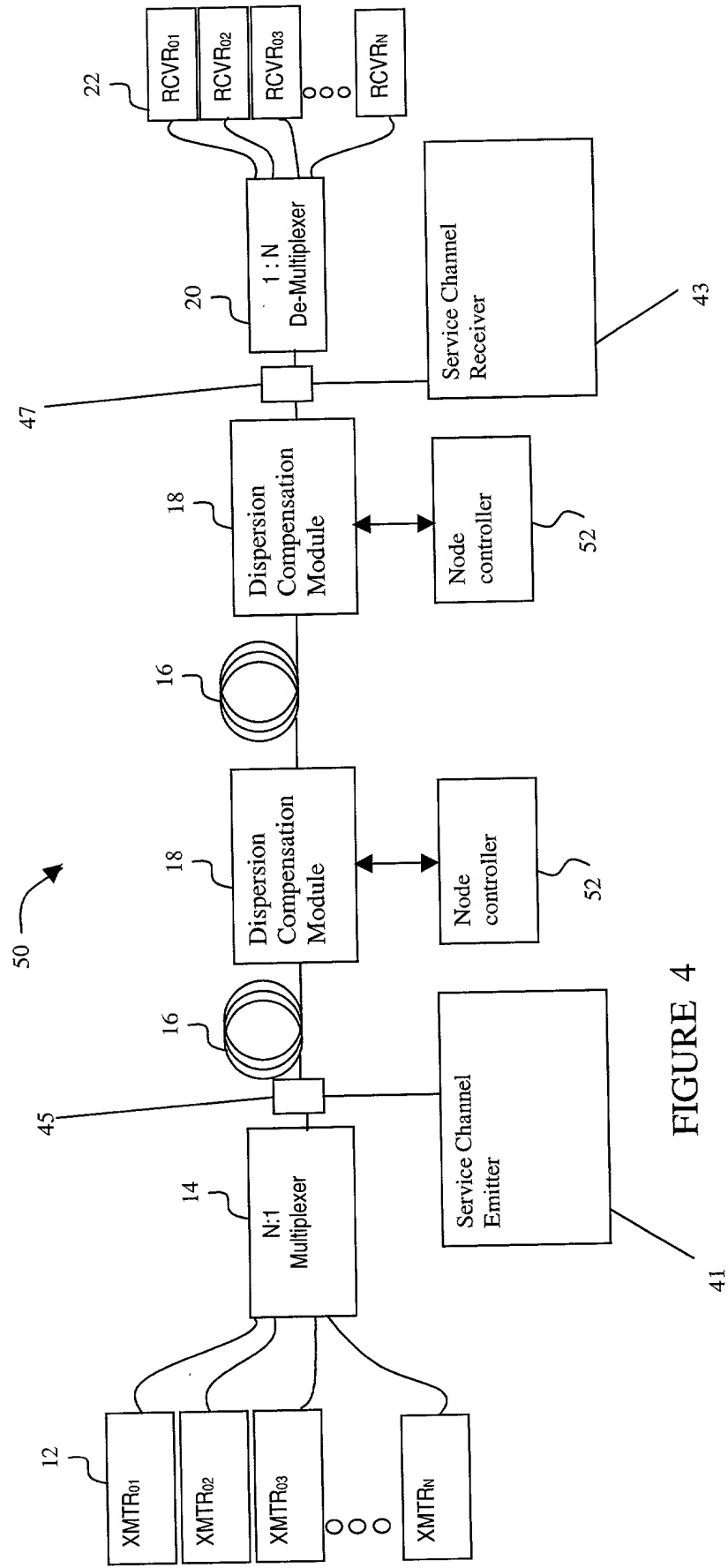


FIGURE 4

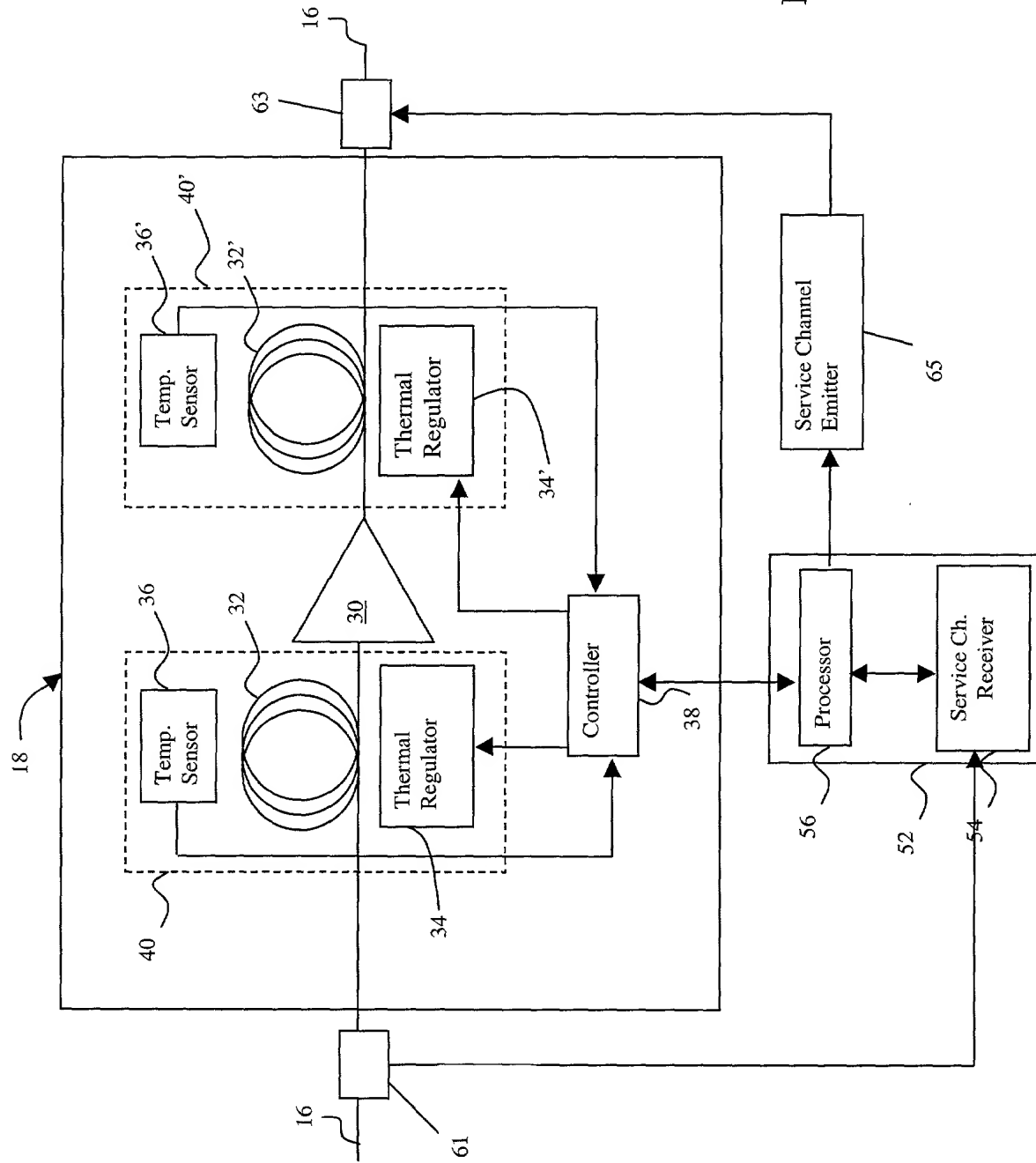


FIG. 5

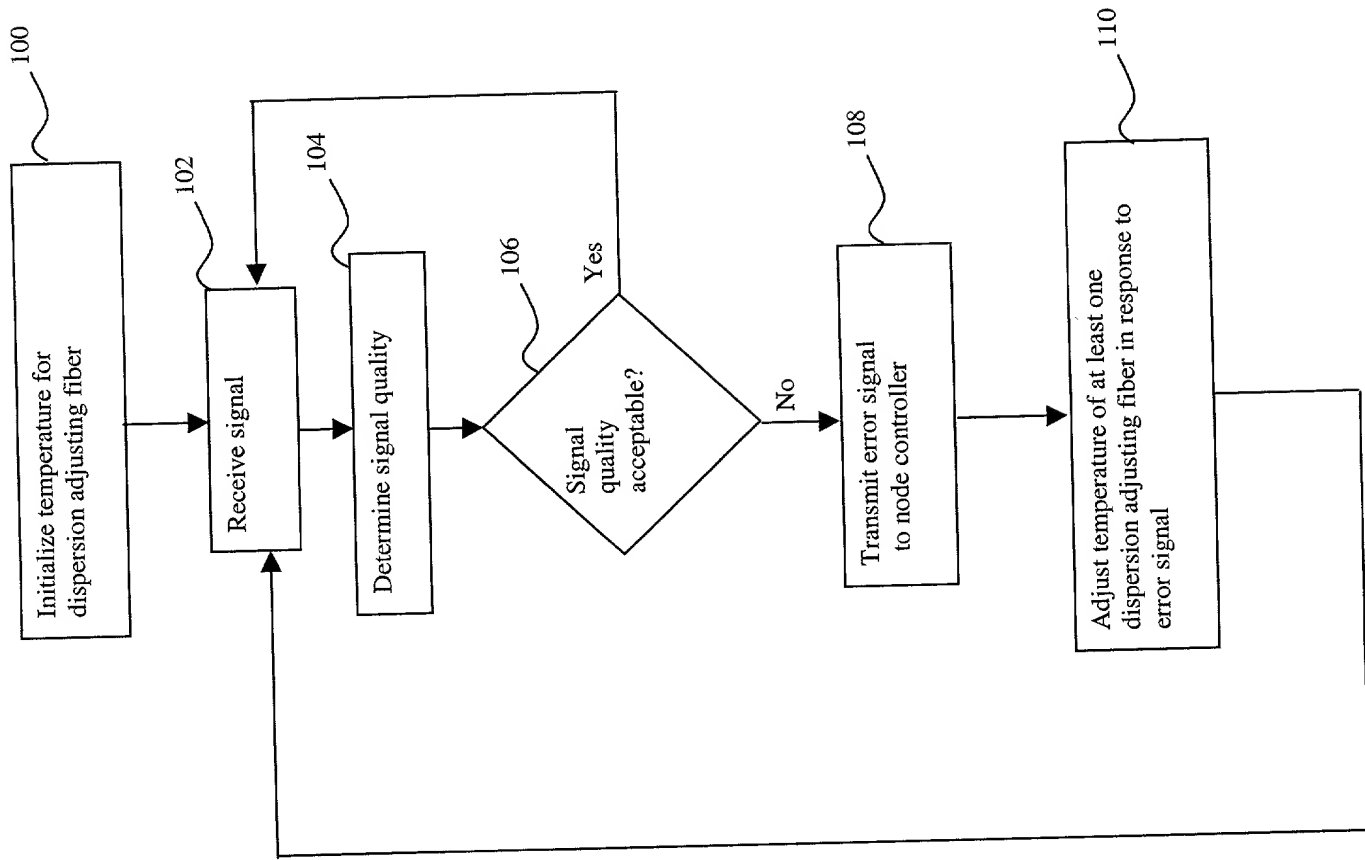


FIG. 6

FIG. 7 is a block diagram of a system 16 for controlling a thermal system 18. The system 16 includes a controller 38, a processor 56, a service channel emitter 65, and a service channel receiver 54. The thermal system 18 includes two thermal regulators 34 and 34', each with a temperature sensor 36 and a thermal regulator 34. The system 16 is connected to a power source 71 and a ground 73 via a line 72. The system 16 is also connected to a service channel 52 via a line 54.

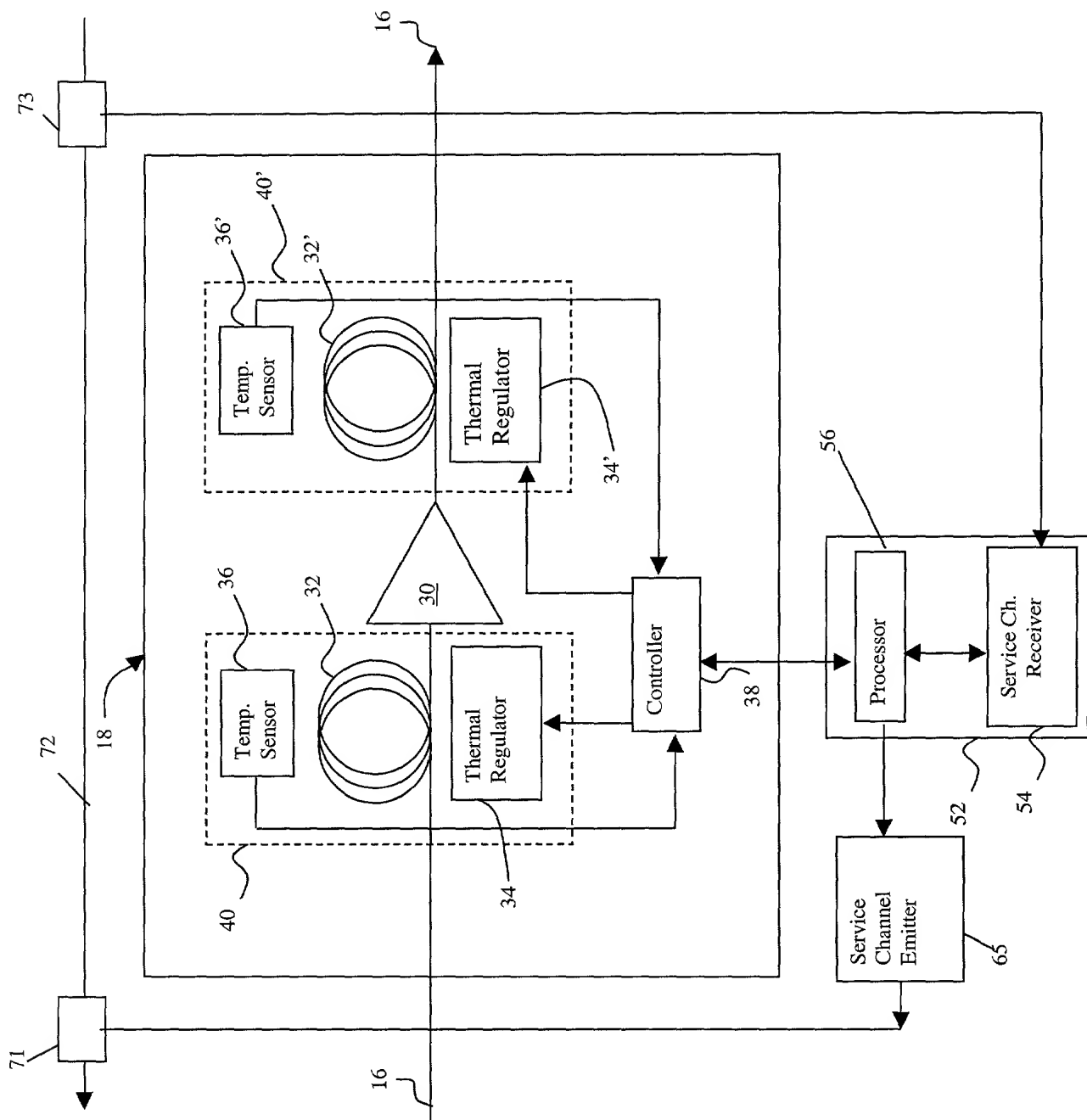


FIG. 7